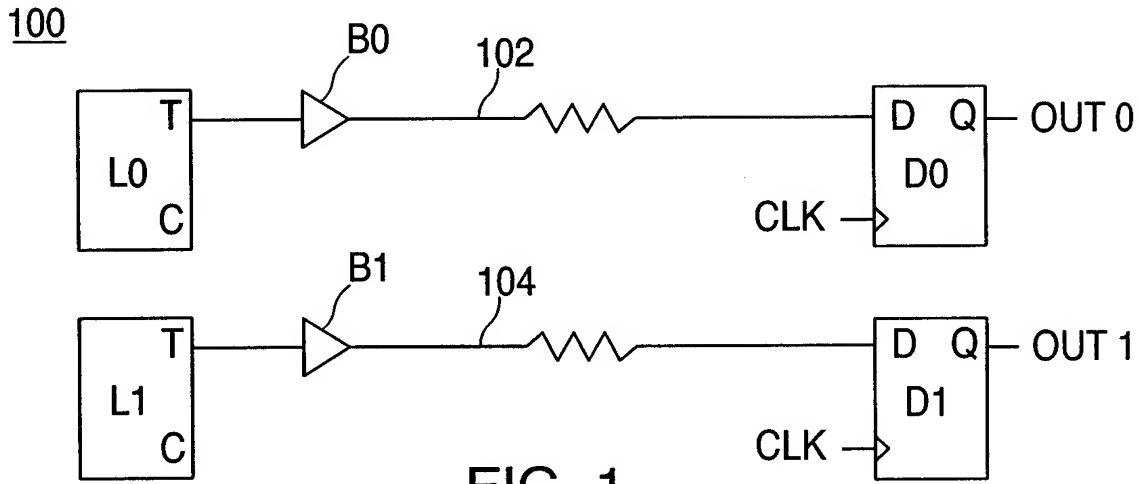


FIG. 1
PRIOR ART

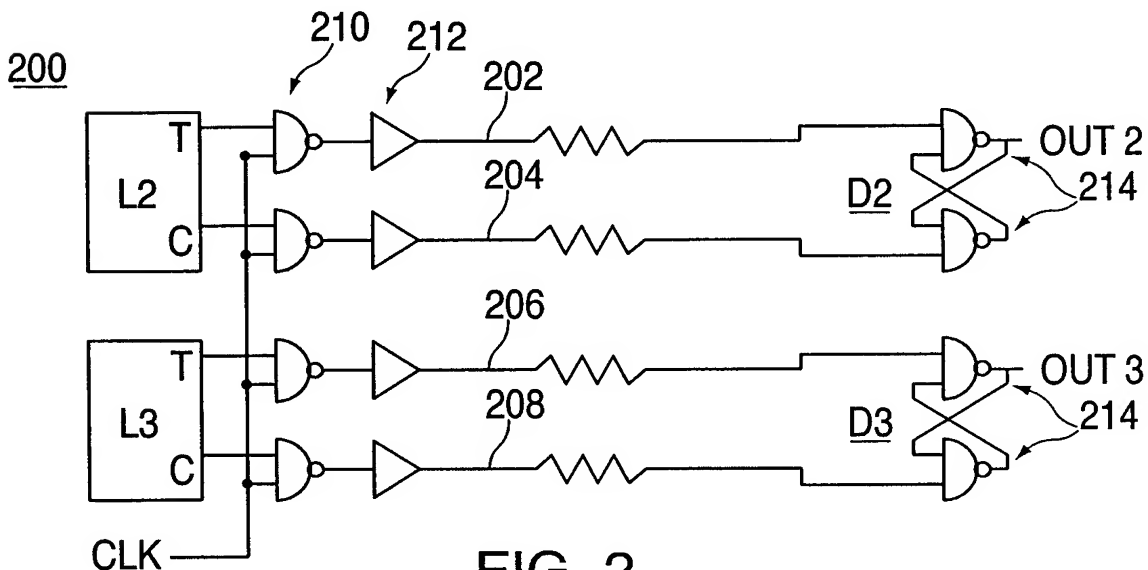


FIG. 2
PRIOR ART

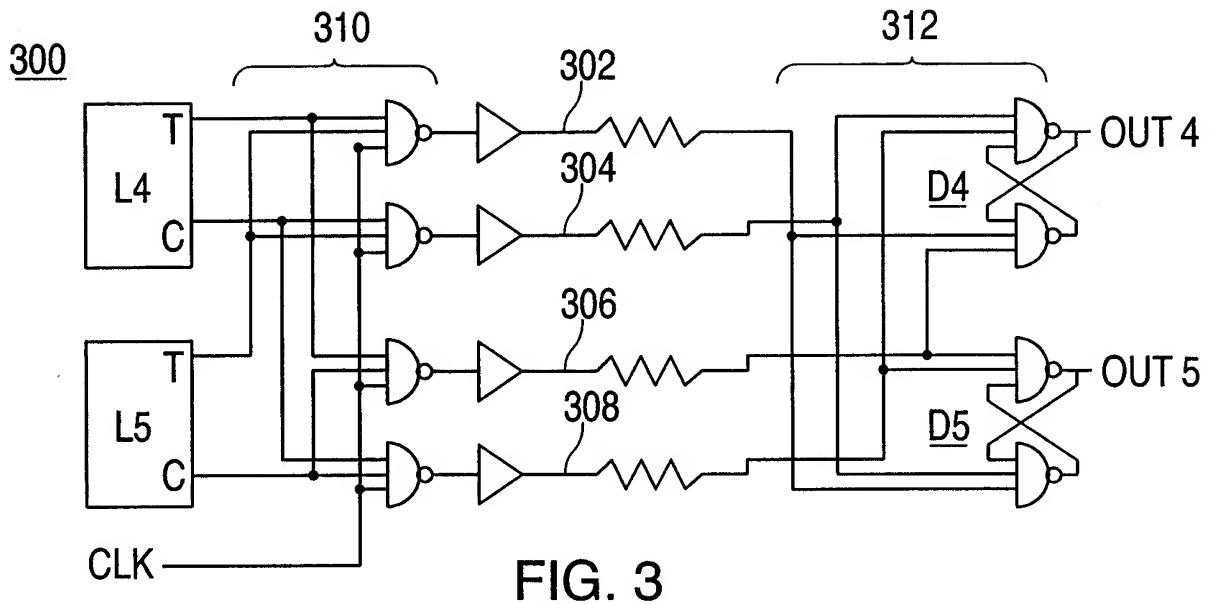


FIG. 3

| L5 | L4 | DATA LINE DISCHARGED (CLK = 1) | RECEIVING LATCH OPERATION | |
|---|---|---|---|---|
| 0 | 0 | 308 | SET D5 | SET D4 |
| 0 | 1 | 306 | SET D5 | RESET D4 |
| 1 | 0 | 304 | RESET D5 | SET D4 |
| 1 | 1 | 302 | RESET D5 | RESET D4 |

FIG. 4

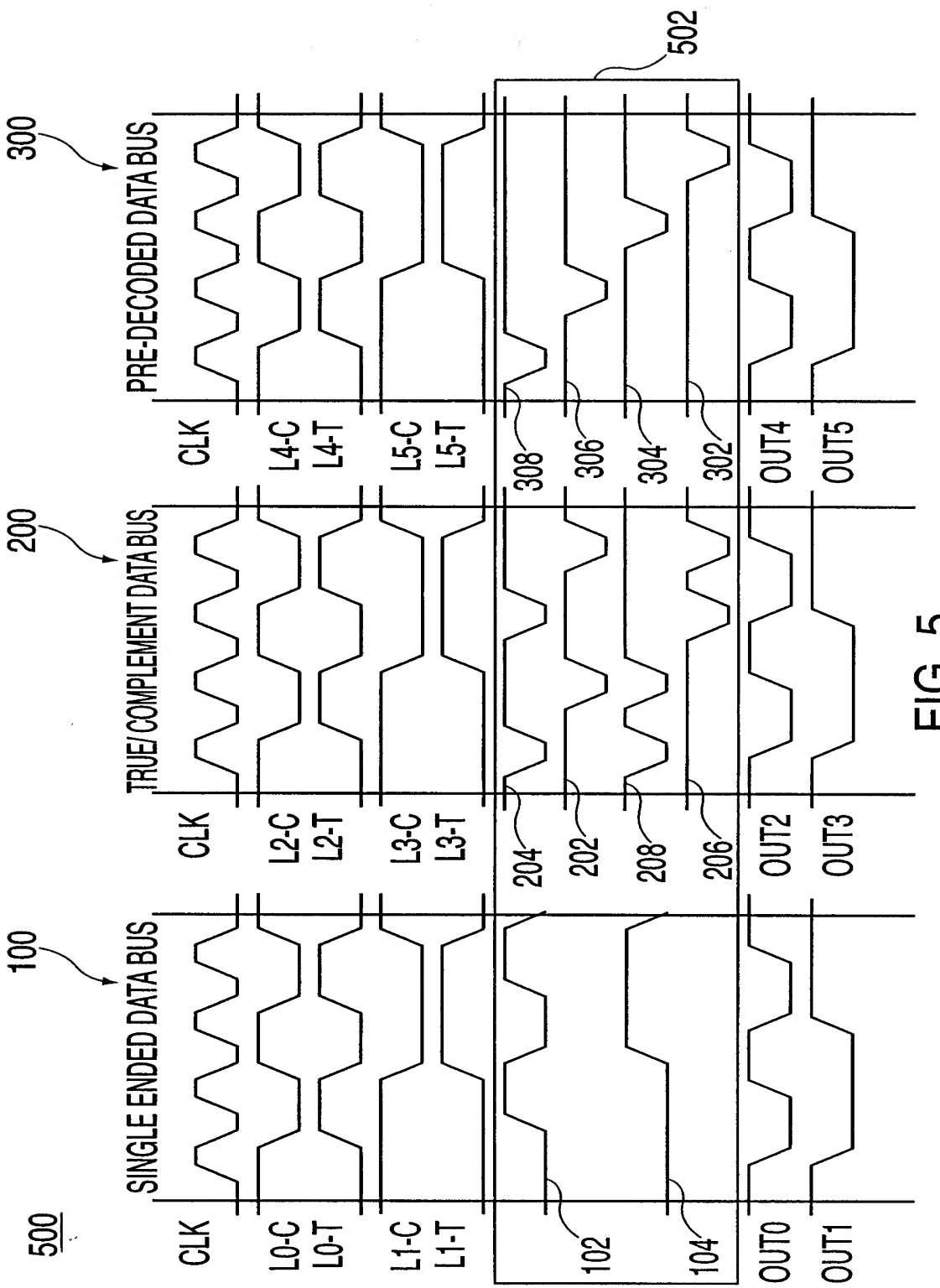
500
100
SINGLE ENDED DATA BUS
CLK
L0-C
L0-T
L1-C
L1-T
102
104
200
TRUE/COMPLEMENT DATA BUS
CLK
L2-C
L2-T
L3-C
L3-T
204
202
208
206
300
PRE-DECODED DATA BUS
CLK
L4-C
L4-T
L5-C
L5-T
308
306
304
302
502
OUT0
OUT1
OUT2
OUT3
OUT4
OUT5

FIG. 5